

## SOLUTION KEY

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### **Problem 1:** (8 points)

- (a) If initially  $R1 = -8$  and  $R2 = 8$ , what is the value of the PC after the following instructions are executed?

x3008:	0101 011 001 000 010	AND r3 r1 r2
x3009:	0000 010 000 000 100	brz pc + 4

***PC = x300A***

- (b) What is the final value of R2 after this program executes? ( $R2 = 1$ ,  $R4 = 6$  initially)

x4001:	0001 010 010 000 010	Add r2,r2,r2
x4002:	0001 100 100 111 111	Add r4,r4,#-1
x4003:	0000 011 111 111 101	BRzp here

***128***

### **Problem 2:** (8 points)

If the number of registers in LC3 is doubled, what would be the effect on:

- (a) The ADD immediate instruction

***Imm is 3 bit: range -4 to 3***

- (b) The range of addresses a JUMP instruction can have

***No Change***

### **Problem 3:** (8 points)

How will you implement the following operation in LC3?

$R4 = R1 \text{ NOR } R2$

Write the machine code (binary 16 bit instructions) in the space below.

<b><i>NOT R1, R1</i></b>	<b><i>1001 001 001 111 111</i></b>
<b><i>NOT R2, R2</i></b>	<b><i>1001 010 010 111 111</i></b>
<b><i>AND R4, R1, R2</i></b>	<b><i>0101 100 001 000 010</i></b>

**Problem 4: (10 points)**

Shown here are the contents of memory before and after the LC3 instruction at location x3010 is executed. Your job: Identify the instruction stored in x3010. Note: There is enough information below to uniquely specify the instruction at x3010.

	Before	After
R0	x3208	x3208
R1	x2d7c	x2d7c
R2	xe373	xe373
R3	x2053	x2053
R4	x33ff	x33ff
R5	x3f1f	x3f1f
R6	xf4a2	xf4a2
R7	x5220	x5220
...		
x3400	x3001	x3001
x3401	x7a00	x7a00
x3402	x7a2b	x7a2b
x3403	xa700	xa700
x3404	xf011	xf011
x3405	x2003	x2003
x3406	x31ba	xe373
x3407	xc100	xc100
x3408	xefef	xefef

*x3010: STR R2, R4, #7 (0111010 100 000111 )*

**Problem 5: (6 points)**

Explain in brief, the difference between:

(a) Data errors and logic errors

*Data errors: Input data is different than expected*

*Logic errors: Program is legal but logically wrong*

(b) Trap and jump instructions

*Trap: Target address is obtained via a table, RET causes old PC to be restored*

*Jump: Target address specified by user through a register, PC is not saved*

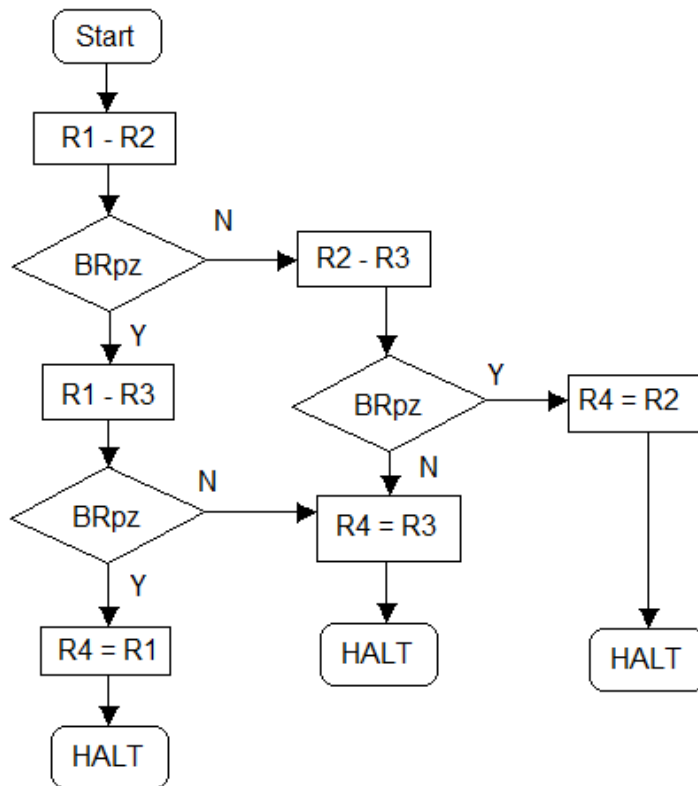
(c) Breakpoints and watchpoints

*Breakpoints: Program stops executing where we set the breakpoint*

*Watchpoints: Execution stops when the variable being watched changes*

**Problem 6: (8 points)**

Let R1, R2 and R3 hold some values. Draw a flowchart to store the greatest of these values in R4. Your flowchart must consist of blocks which correspond to groups of LC3 instructions.



**Problem 7: (8 points)**

We are about to execute the following program:

Address	ISA Instruction
x3000	1110 0000 0000 1110 ; LEA R0, x00B
x3001	0010 0010 0000 1110 ; LD R1, x00D
x3002	0110 0100 1100 1110 ; LDR R2, R3, x0B
x3003	1111 0000 0010 0101 ; HALT

The state of the machine before the program starts is given below:

Memory location x300B contains x311B  
Memory location x300E contains xFE12  
Register R0 and R1 contain x0000

Memory location x300C contains xC465  
Memory location x300F contains x1243  
Registers R2 and R3 contain x3000

What will be the final contents of registers R0-R3 when we reach the HALT instruction? Write your answers in hexadecimal format.

***R0 = x300C***

***R1 = x1243***

***R2 = x311B***

***R3 = x3000***

**Problem 8: (4 pts)**

Consider the following snippets of LC3 code which achieve the same function:

(i) LDI R5, LABEL\_1    where LABEL\_1 = x4010

(ii) LD R5, LABEL\_2    where LABEL = x400F  
LDR R5, R5, #1

What is the advantage of using (i) over (ii)?

***More compact***

What is the advantage of using (ii) over (i)?

***More flexible because of the offset***